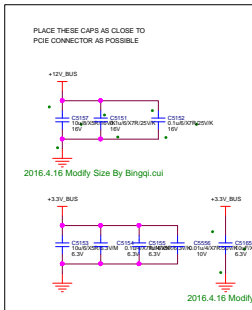
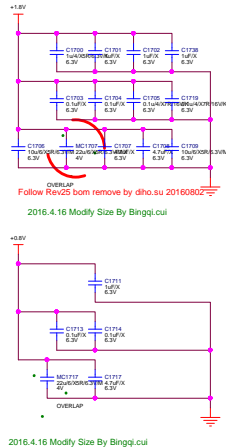


(1) PCI-EXPRESS EDGE CONNECTOR

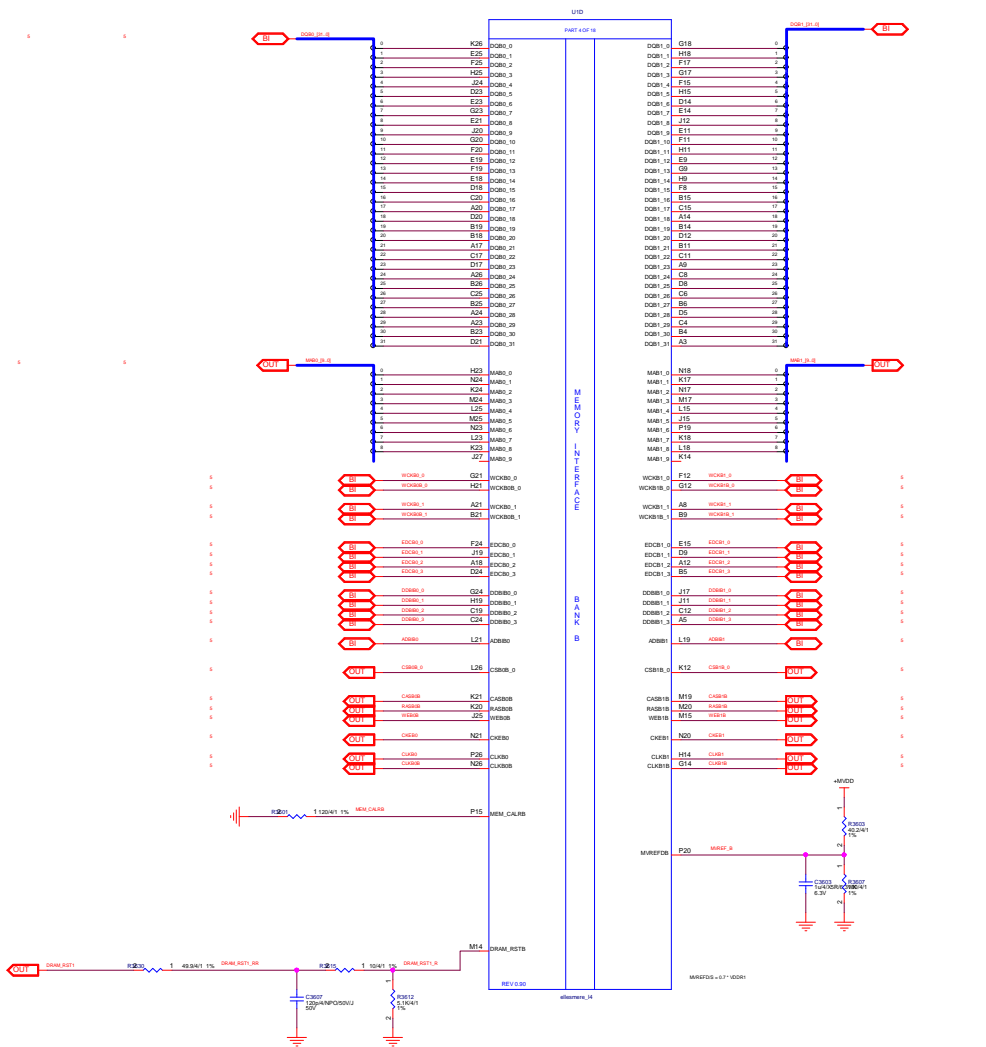
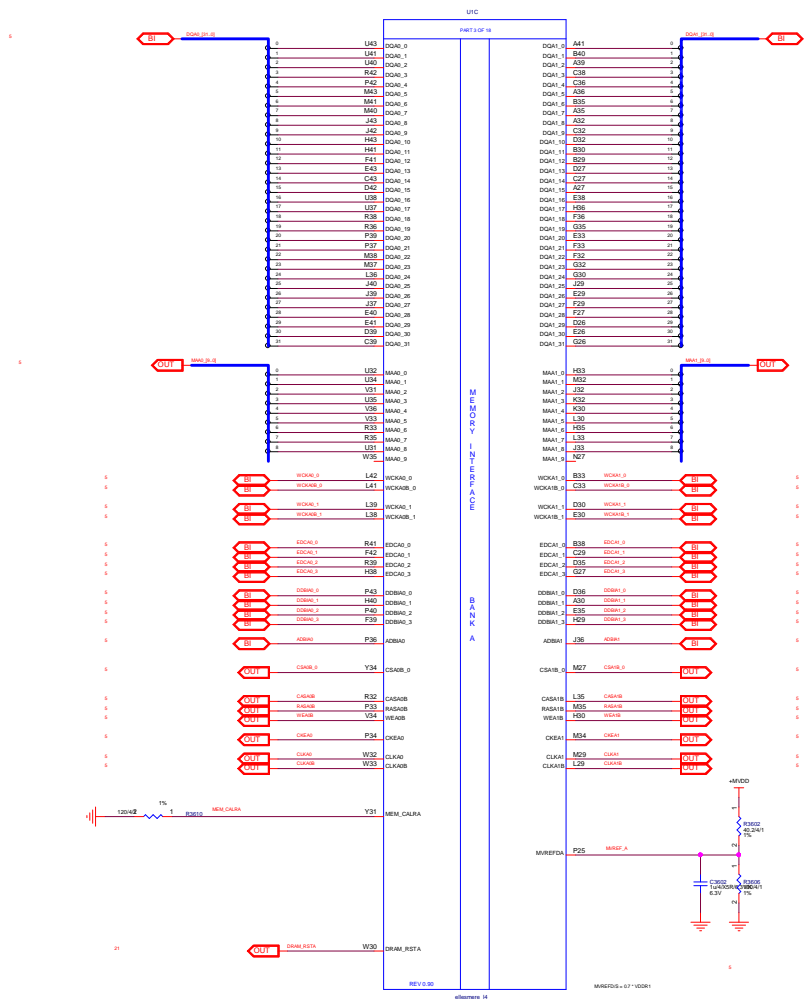


GIGABYTE			
Title PCI Express			
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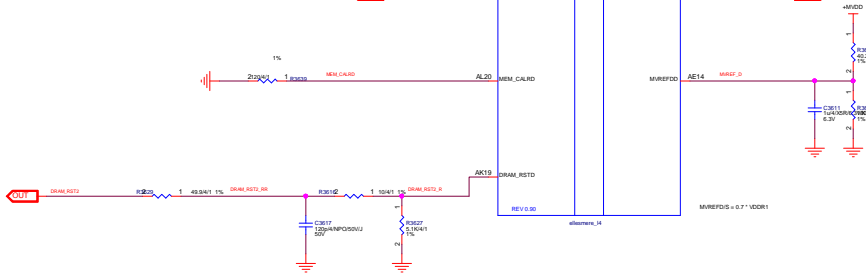
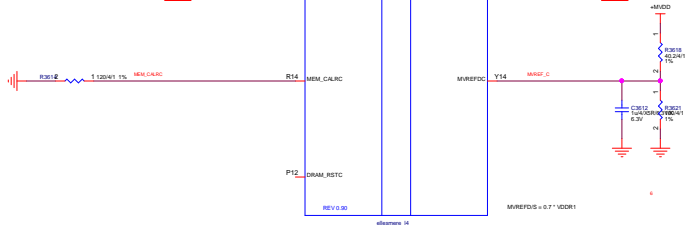
(2) ELLESMERE PCIE INTERFACE



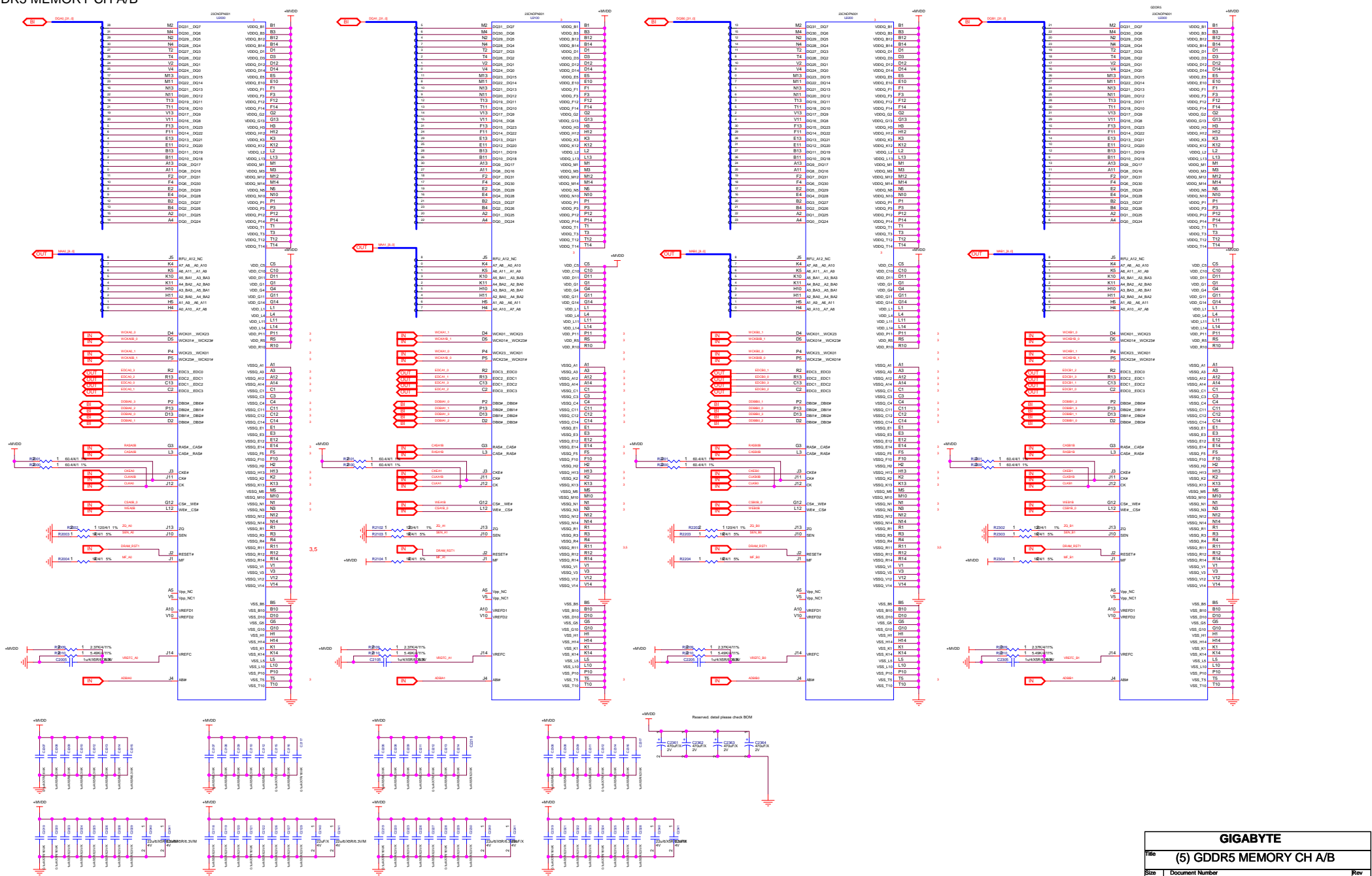
(3) ELLESMERE MEM INTERFACE CH A/B



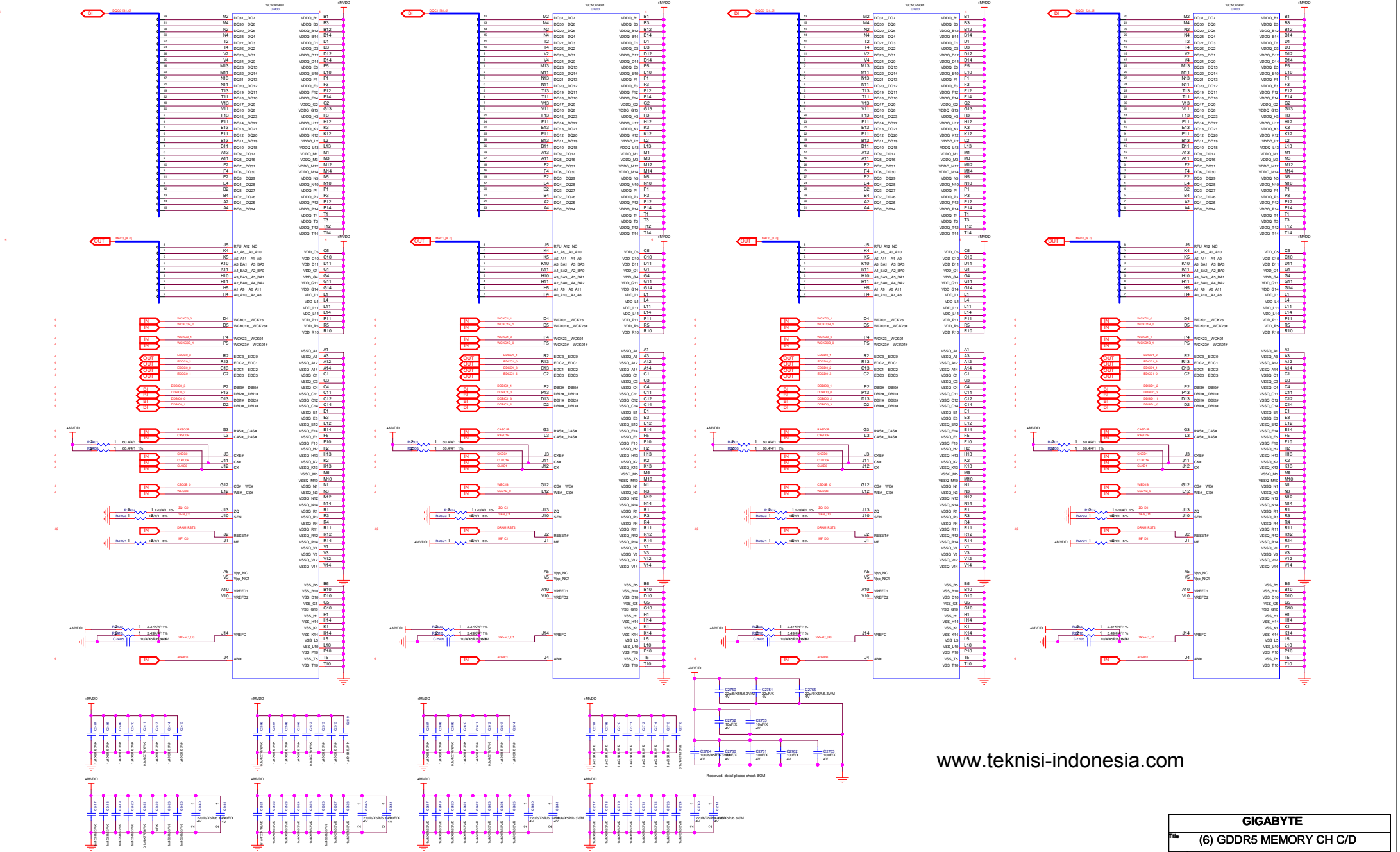
(4) ELLESMERE MEM INTERFACE CH C/D



(5) GDDR5 MEMORY CH A/B



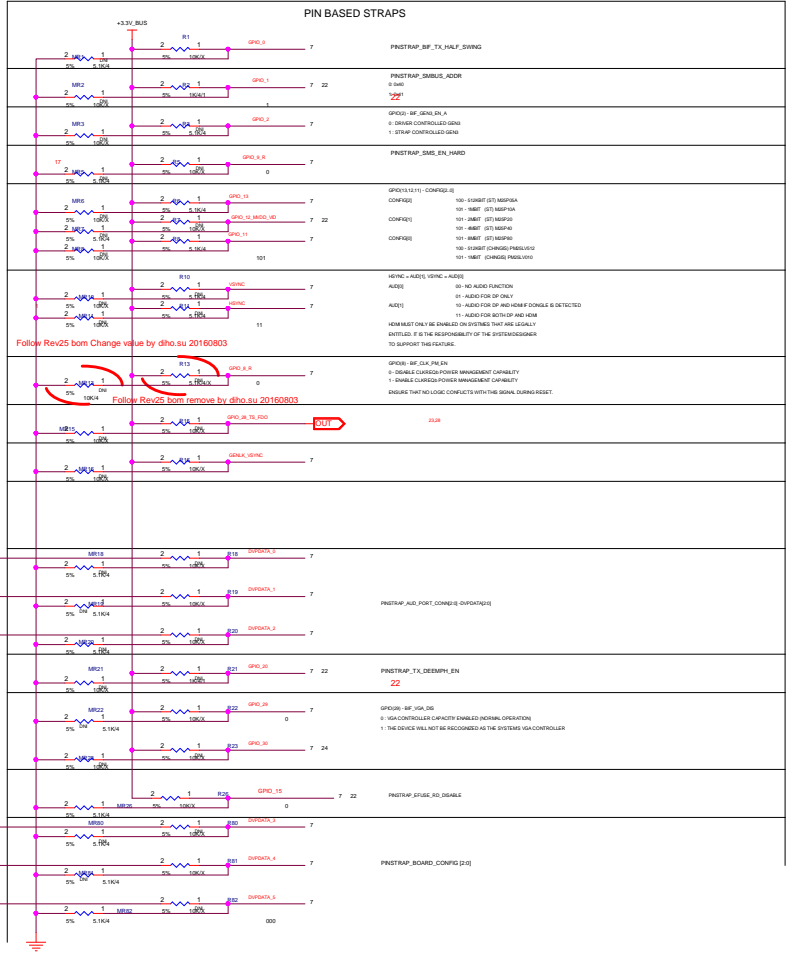
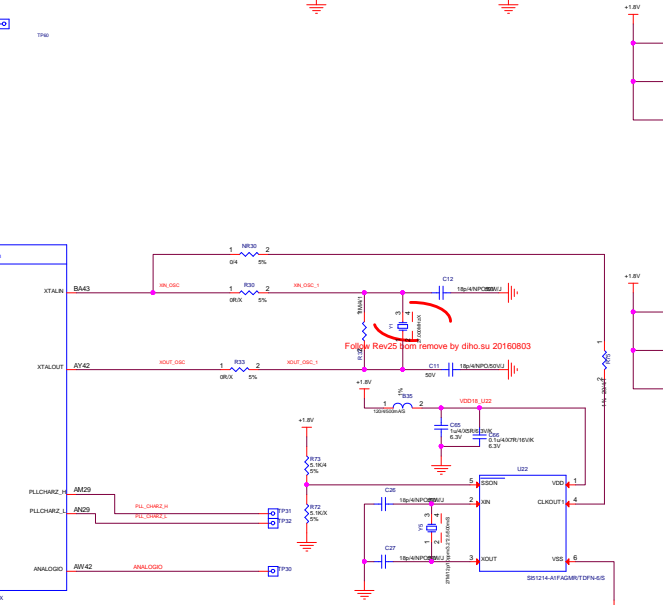
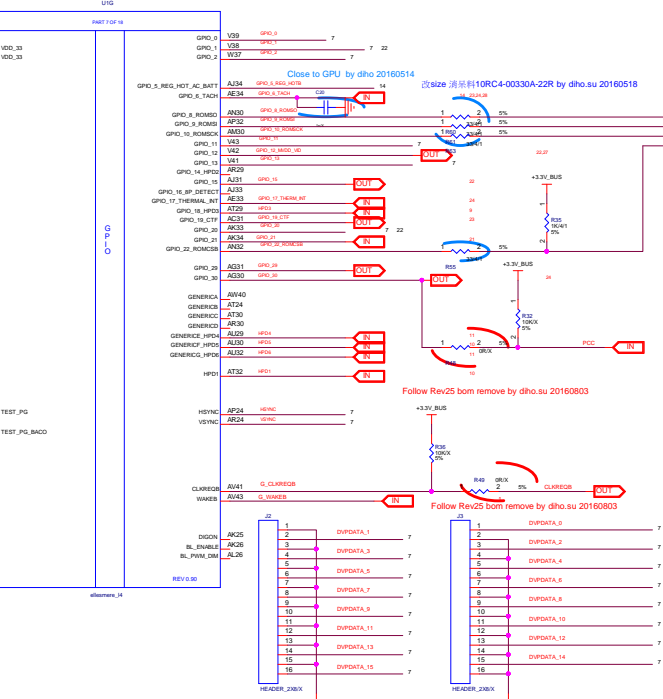
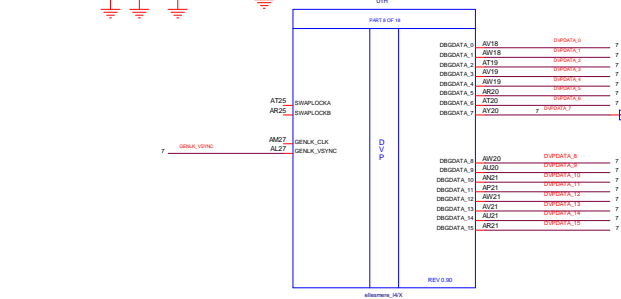
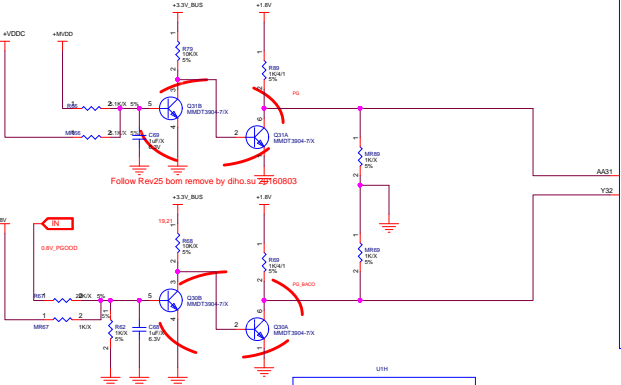
(6) GDDR5 MEMORY CH C/D



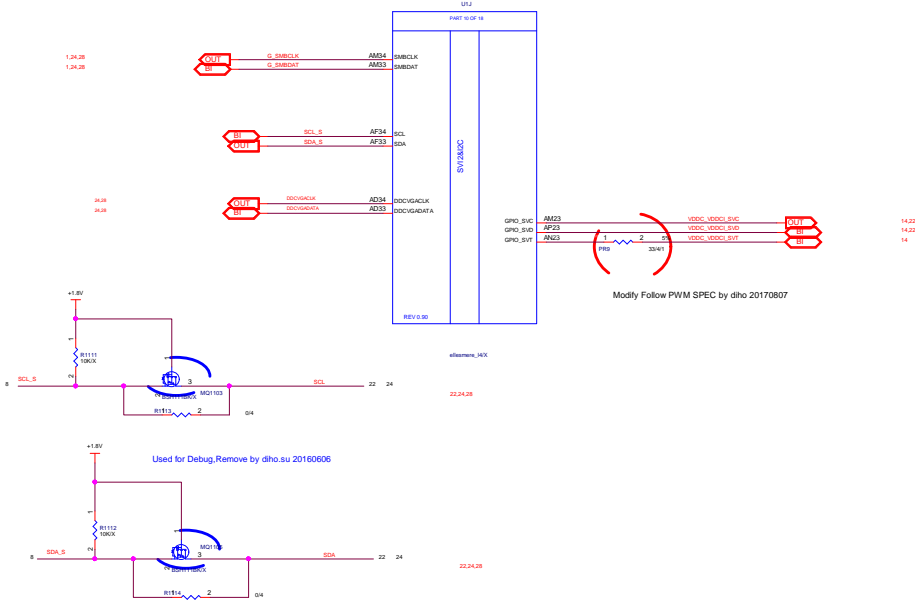
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(7) ELLESMERE GPIO STRAP CF XTAL

I2C/SDA BUS		
IC ADDRESS	FUNCTION	DEVICE
DOCVGA BUS		
IC ADDRESS	FUNCTION	DEVICE
048	EXT TEMP SENSOR	LM8003



(8) ELLESMERE DAC1 LOCK

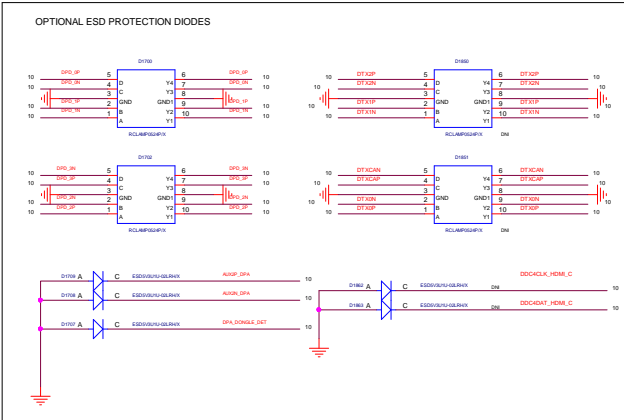
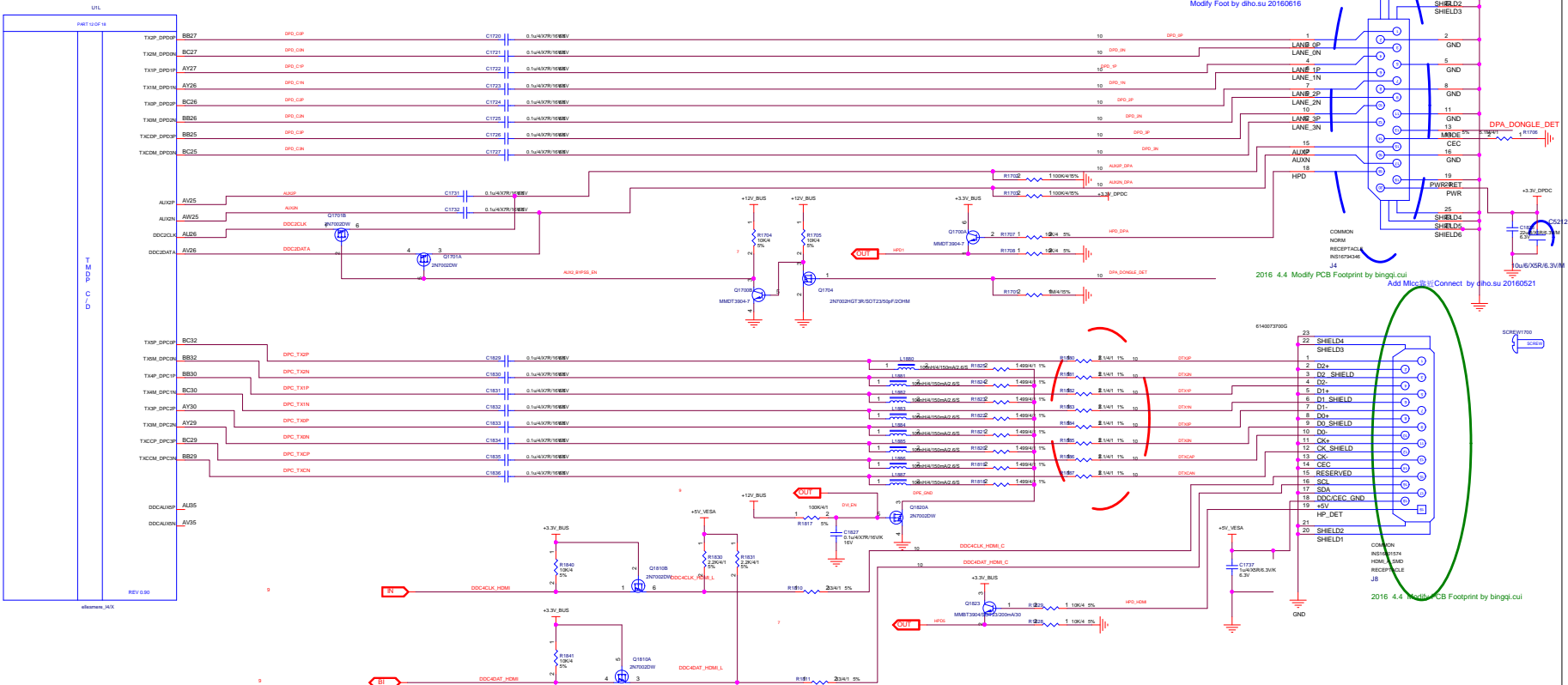


GIGABYTE			
Title ELLESMERE DAC1 LOCK			
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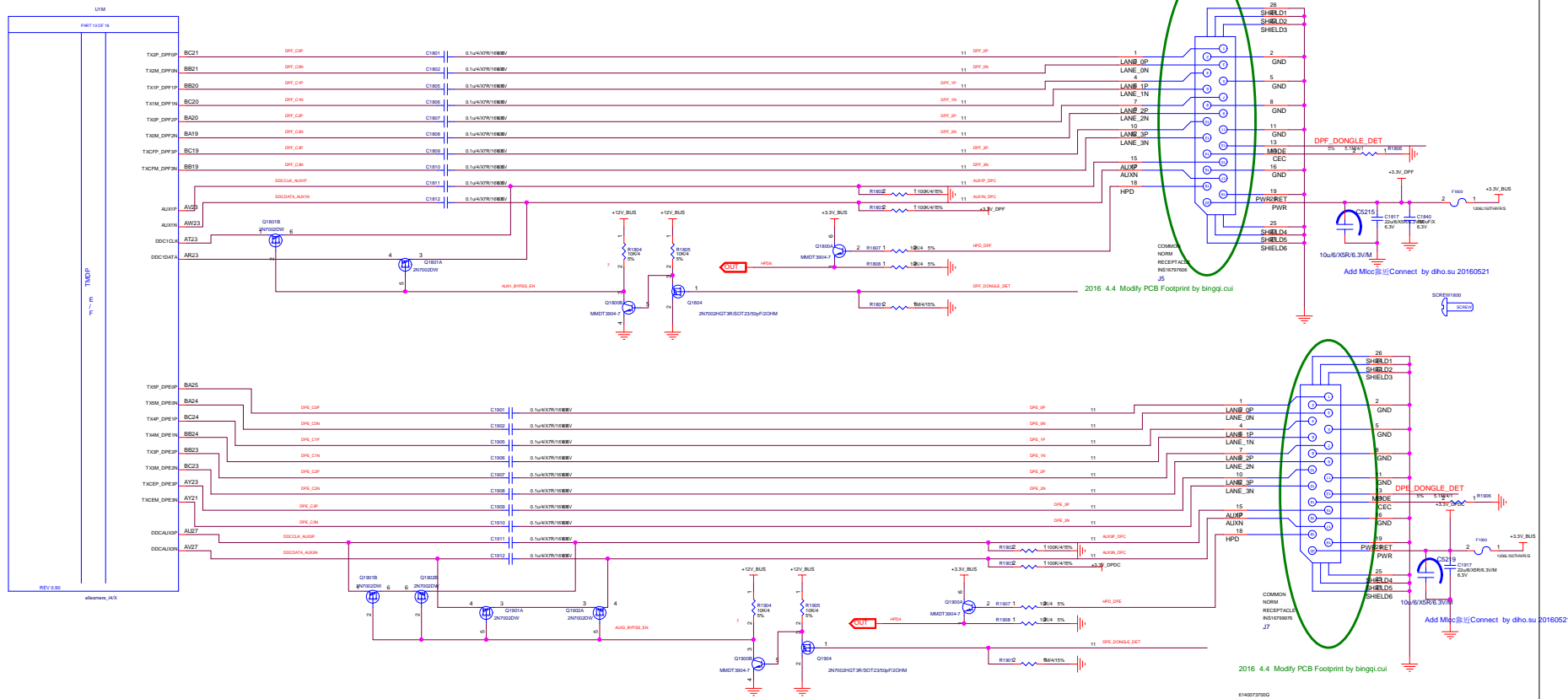
(9) ELLESMERE TMDP A/B

[illegible]

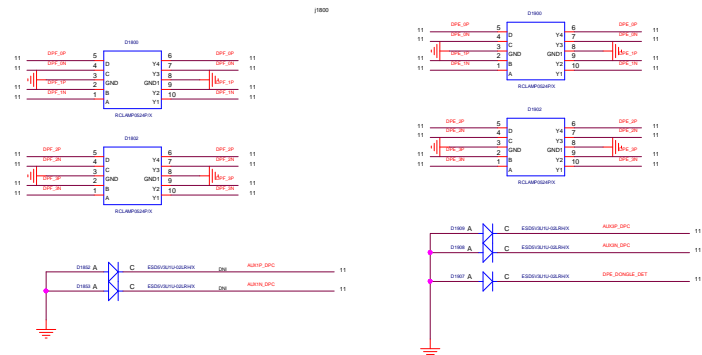
(10) ELLESMERE TMDP C/D



GIGABYTE			
Title ELLESMERE TMDP C/D			
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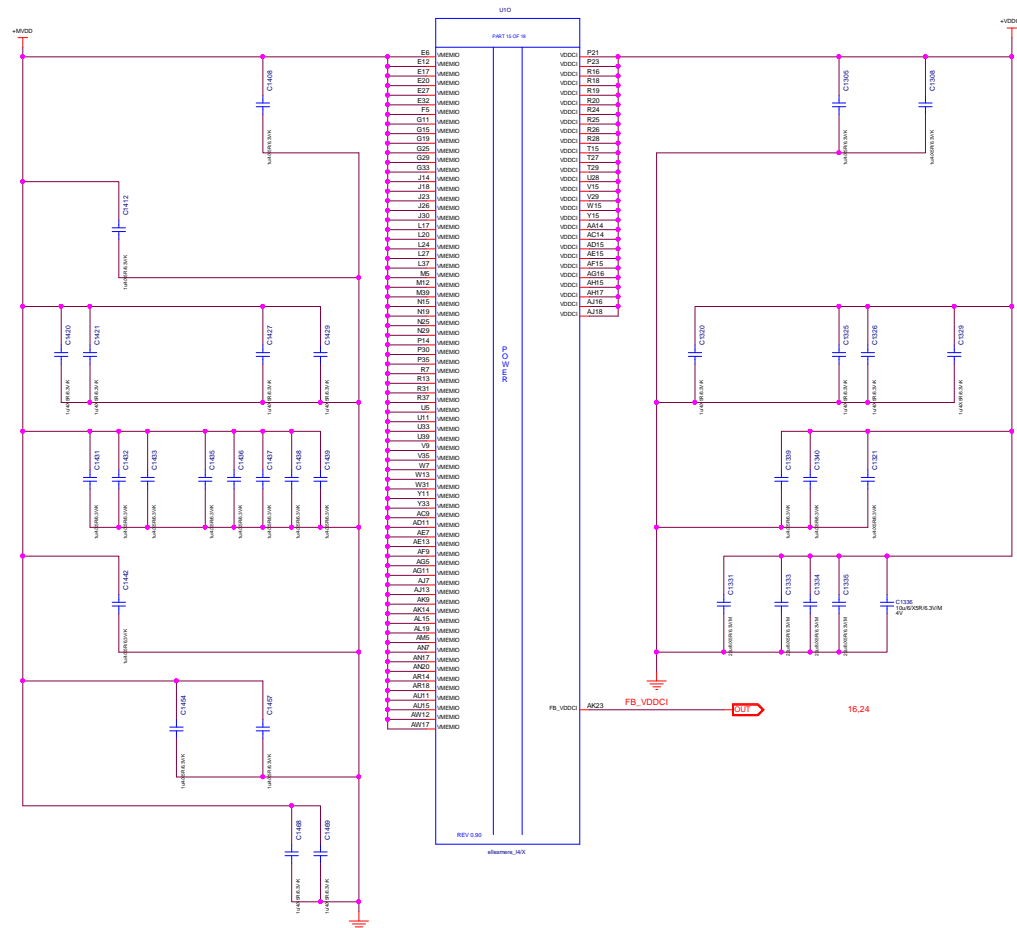
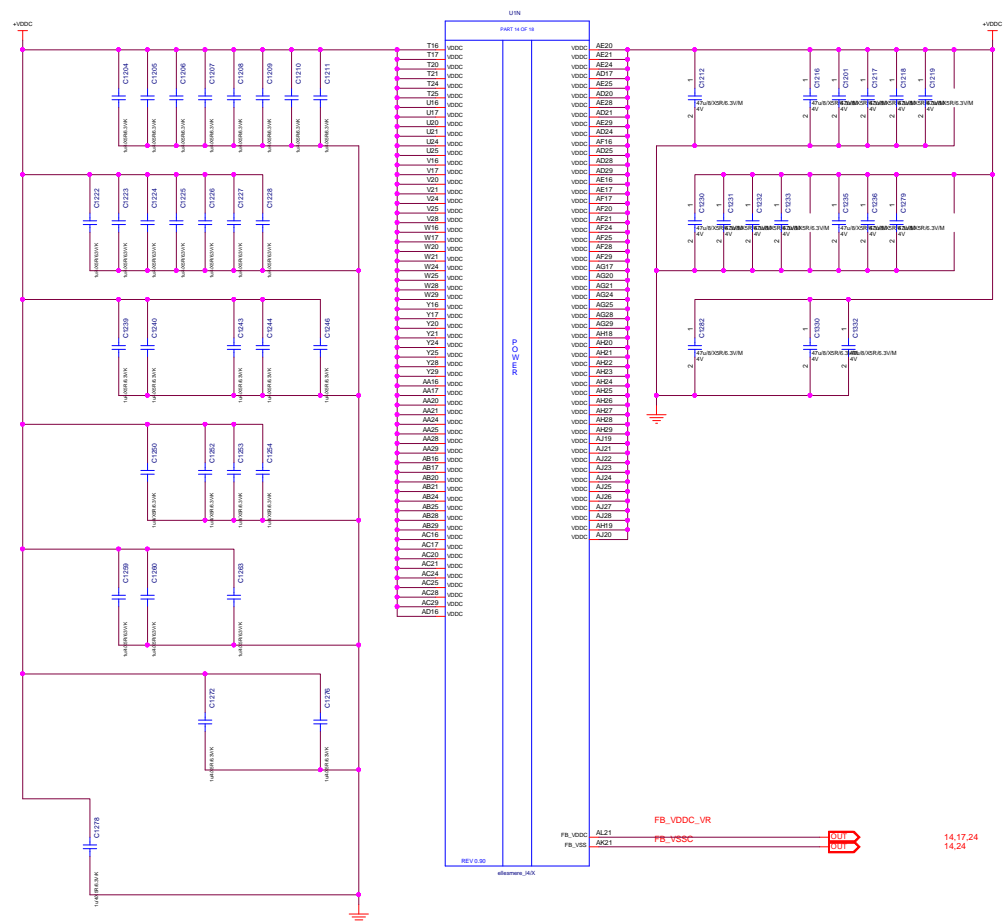


OPTIONAL ESD PROTECTION DIODES

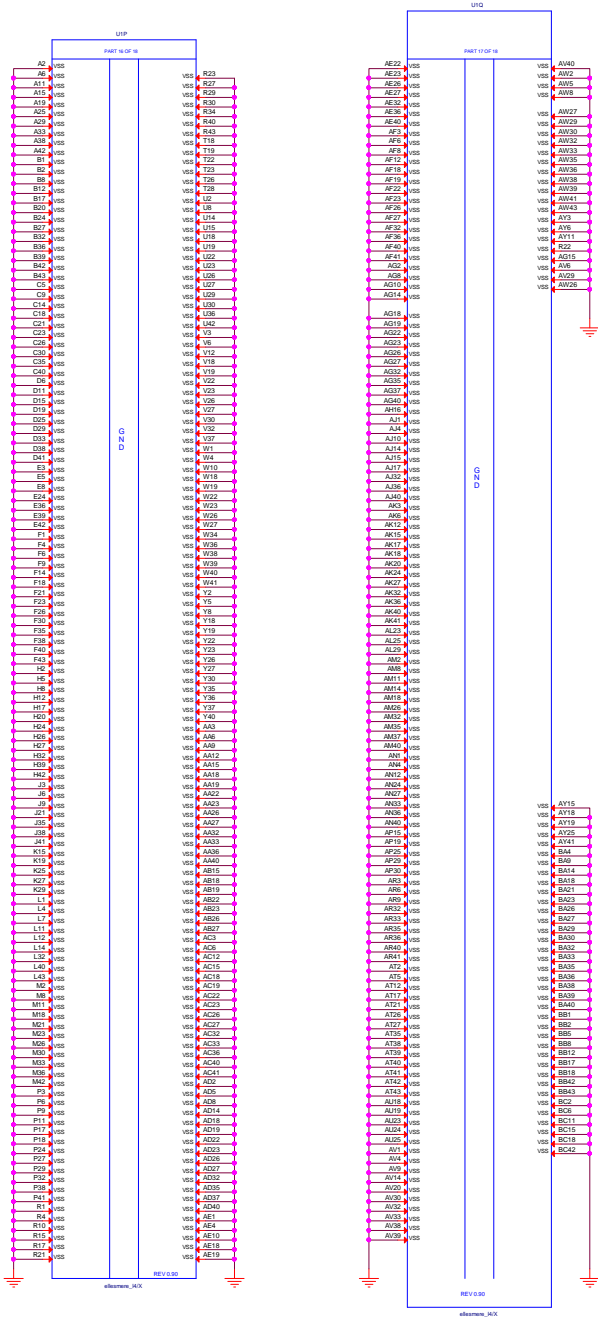


GIGABYTE			
Title ELLESMERE LVTMDP E/F			
Size Custom	Document Number GV-RX590 GME-8GD		Rev 1.0
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(12) ELLESMERE POWER

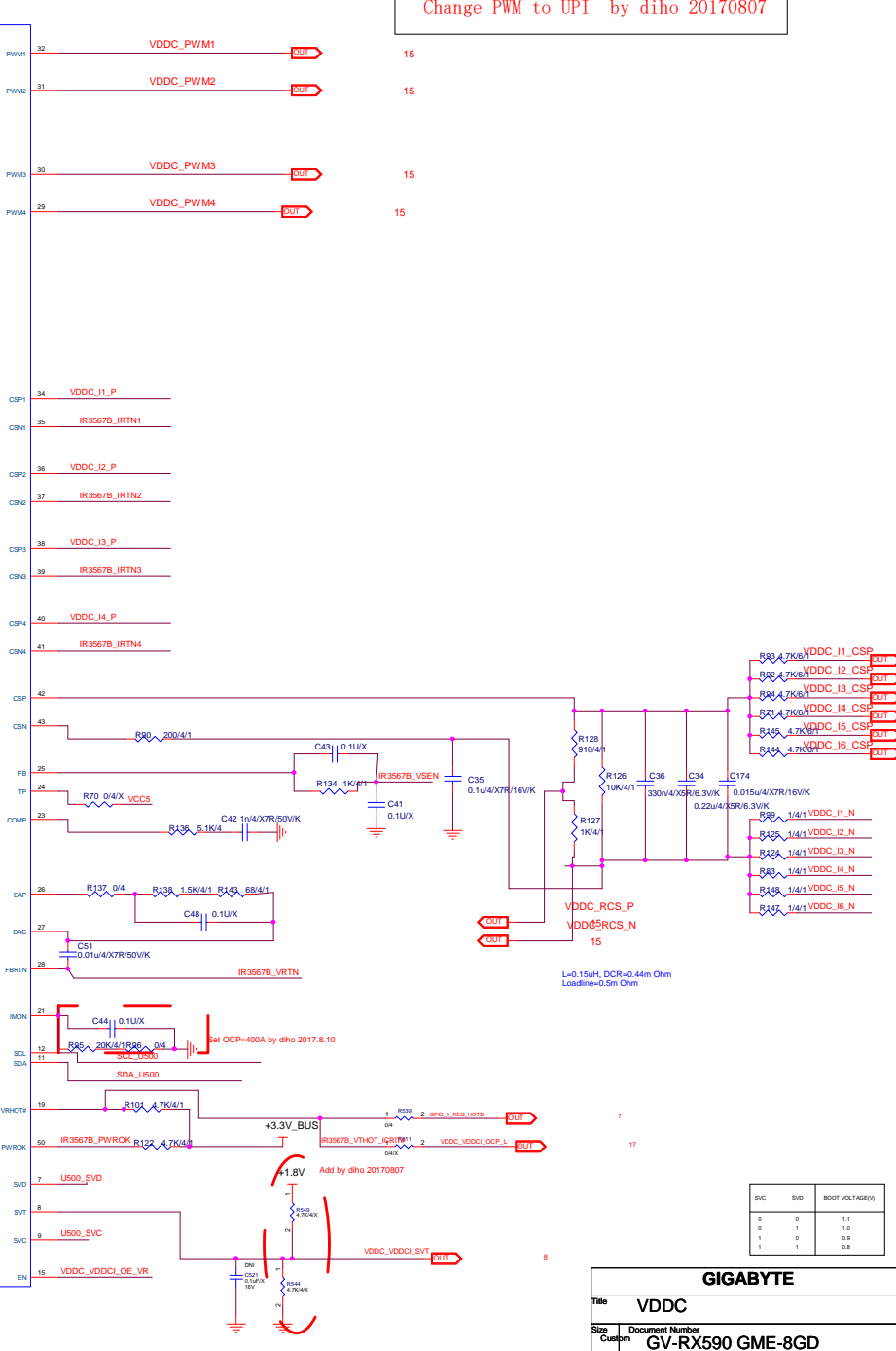
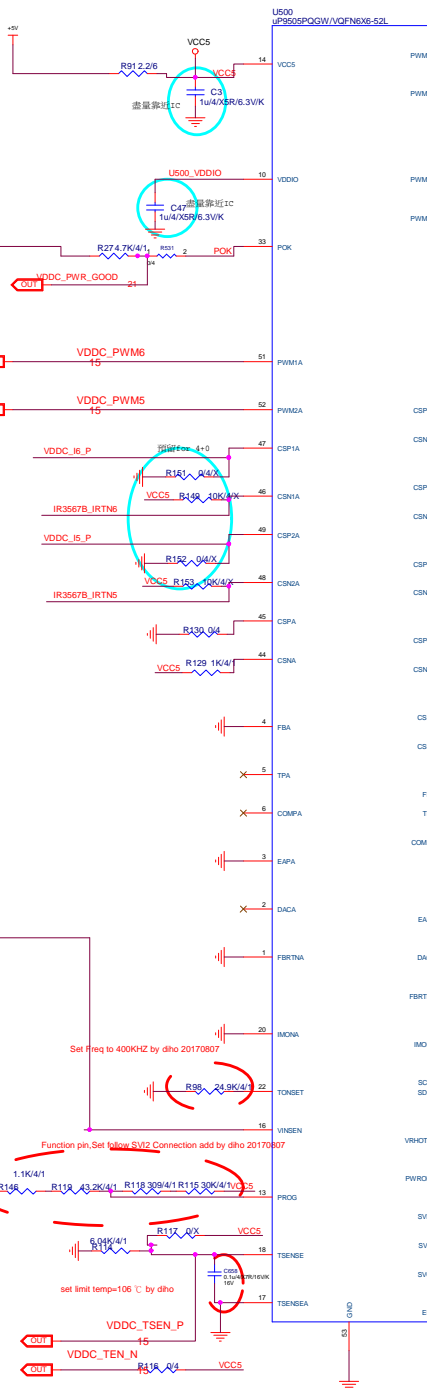
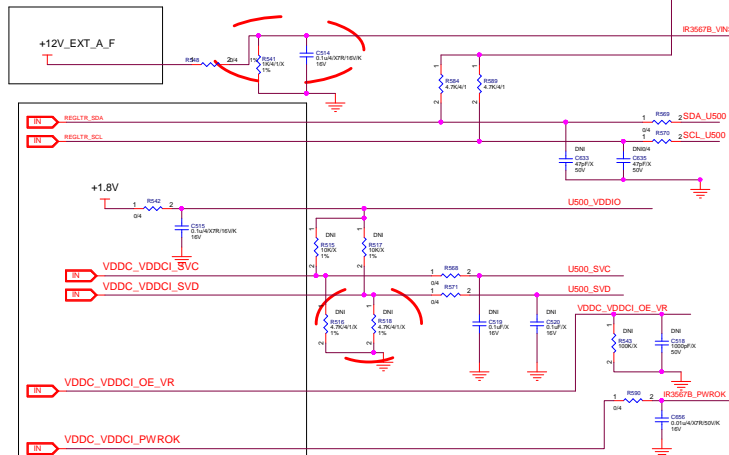
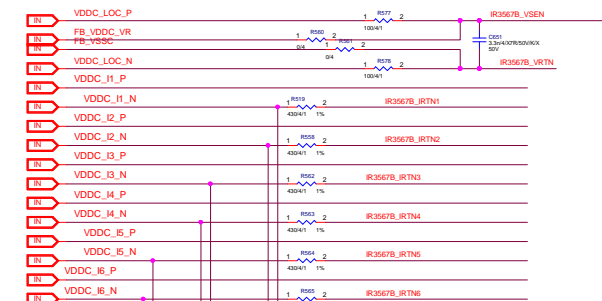


(13) ELLESMERE GROUND



GIGABYTE			
Title ELLESMERE GND			
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+3.3V_BUS



Change PWM to UPI by diho 20170807

SVC	SVD	BOOT VOLTAGE(V)
0	0	1.1
0	1	1.0
1	0	0.9
1	1	0.8

GIGABYTE

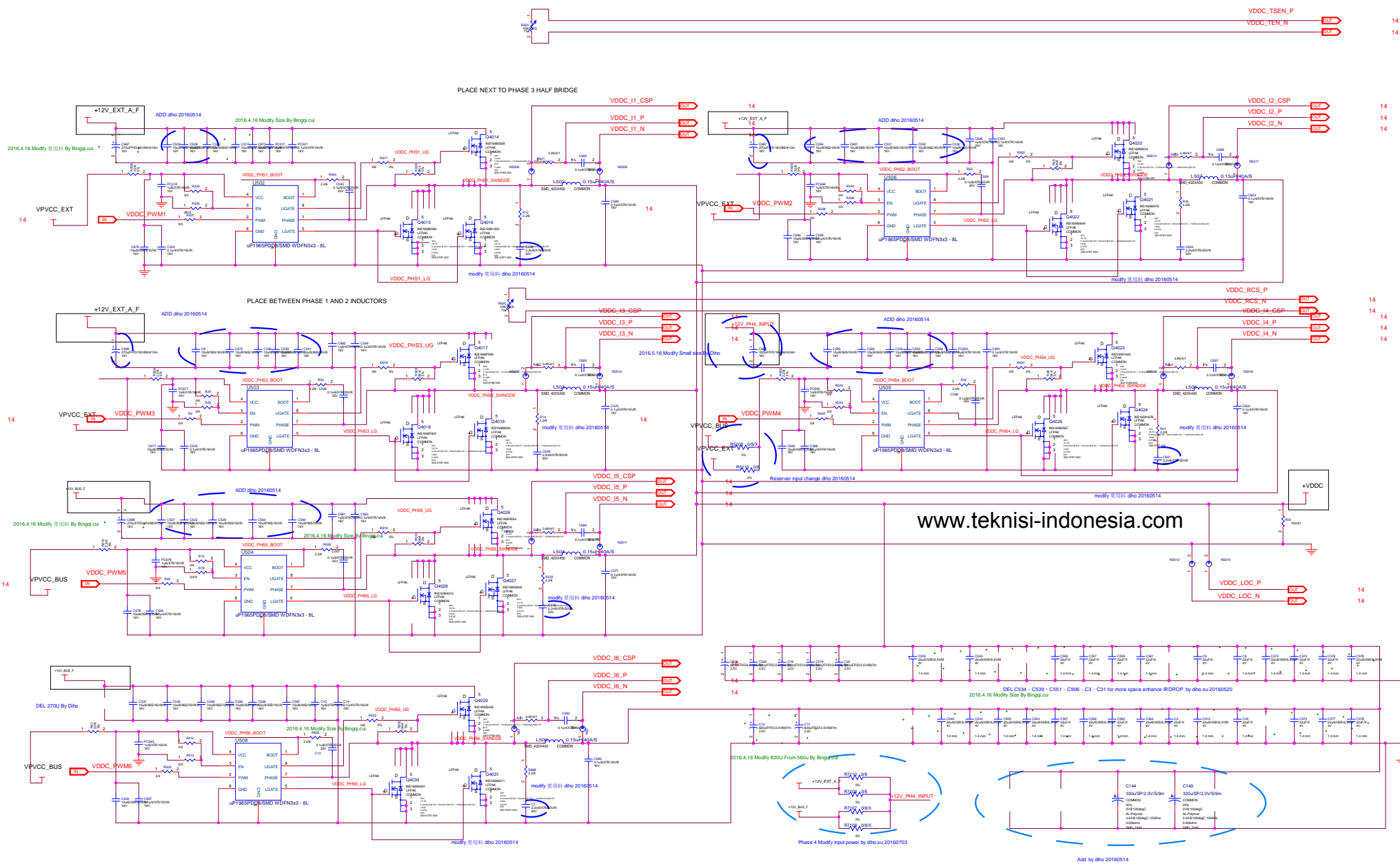
U500

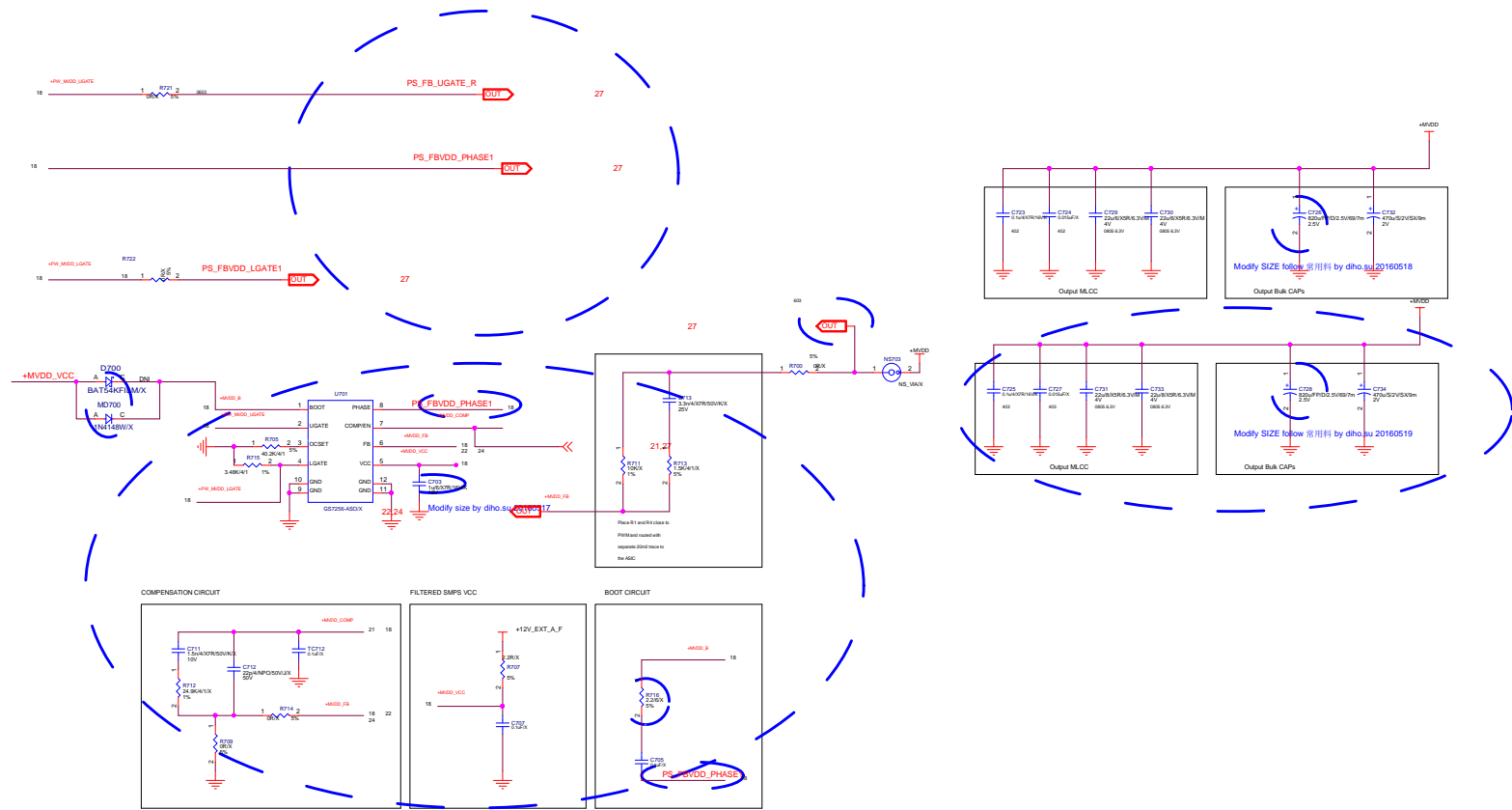
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Document Number: GV-RX590 GME-8GD

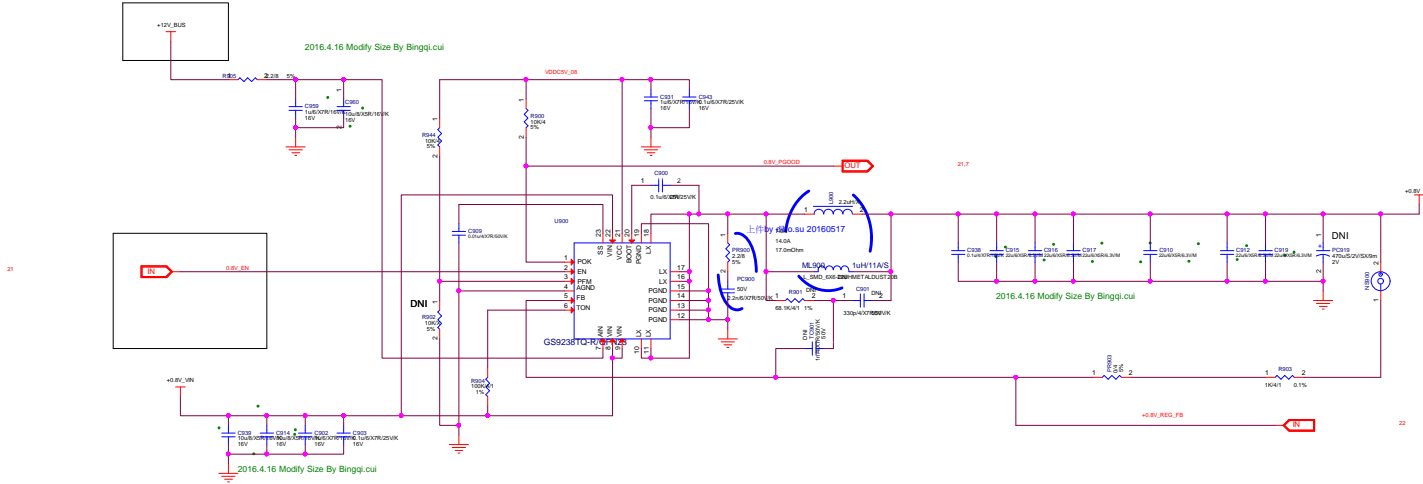
Rev: 1.0

Monday, April 13, 2020





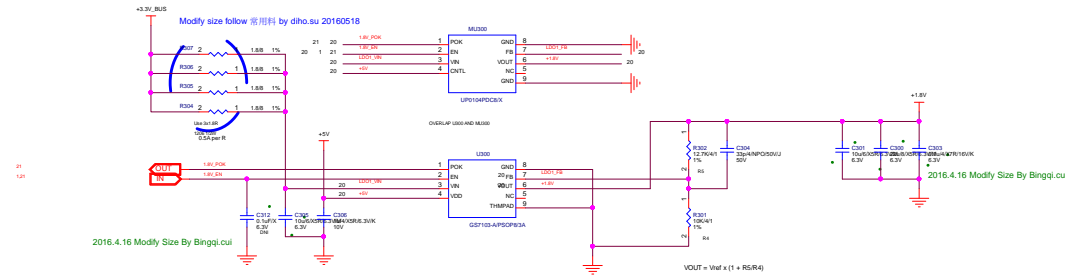
/X Single phase power by diho.su 20160517



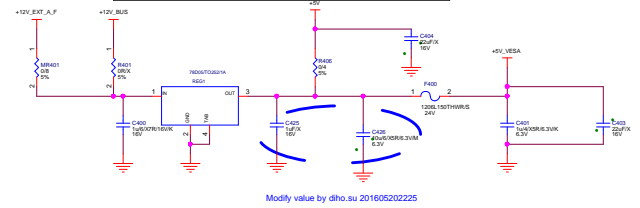
GIGABYTE			
Title 0.95V			
Size	Document Number		Rev
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(18) SMALL RAIL REGULATORS

LDO #1: VIN = 3.0V TO 3.6V MAX
VOUT = +1.8V +/- 2%
IOUT = 1.3A RMS MAX
PCB: 50 TO 70mm SQ. COPPER AREA FOR COOLING

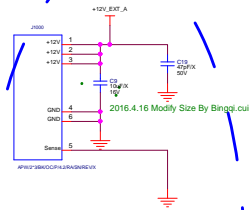


REGULATOR FOR +5V RAILS
IOUT MAX = 150mA

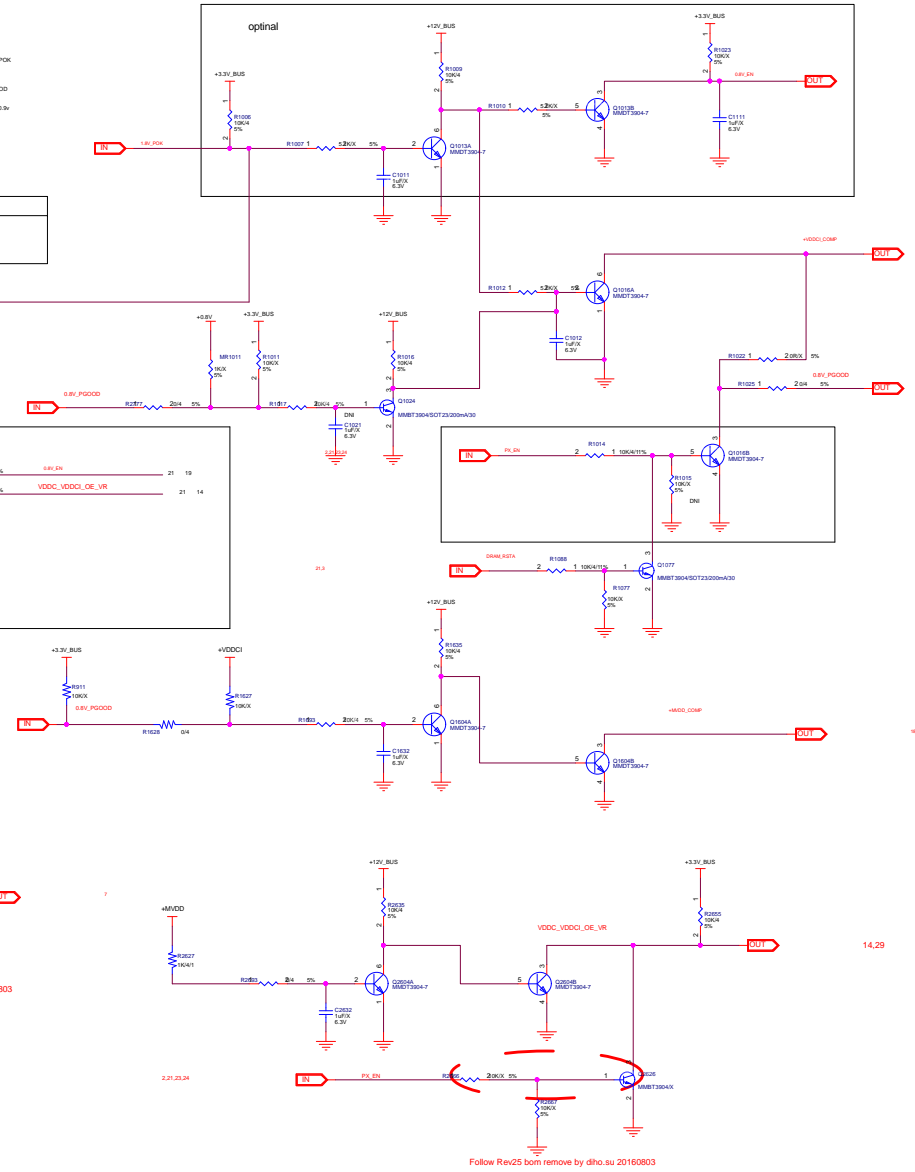
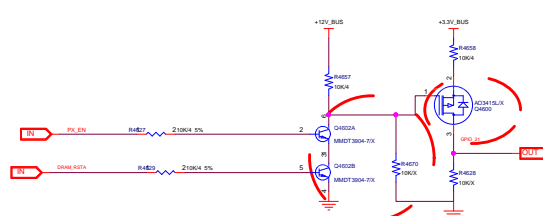
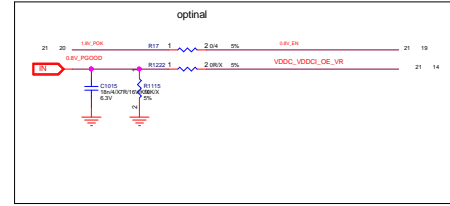
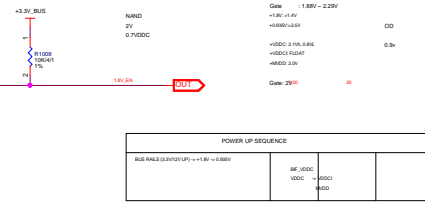
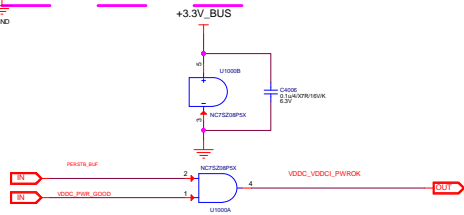
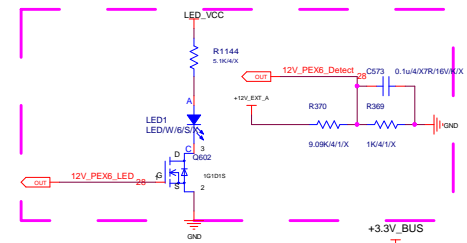
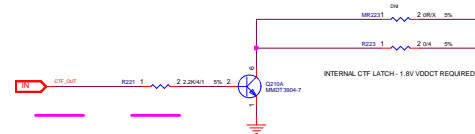
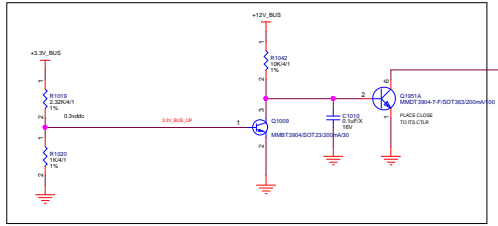
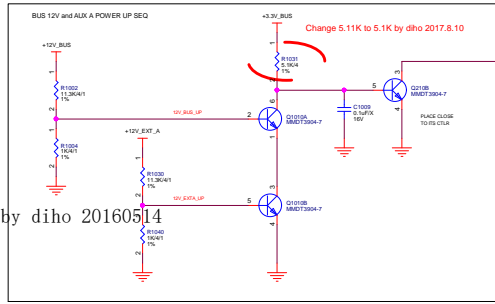
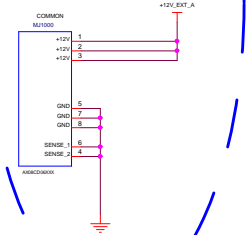


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Title SMALL RAIL REGULATORS				
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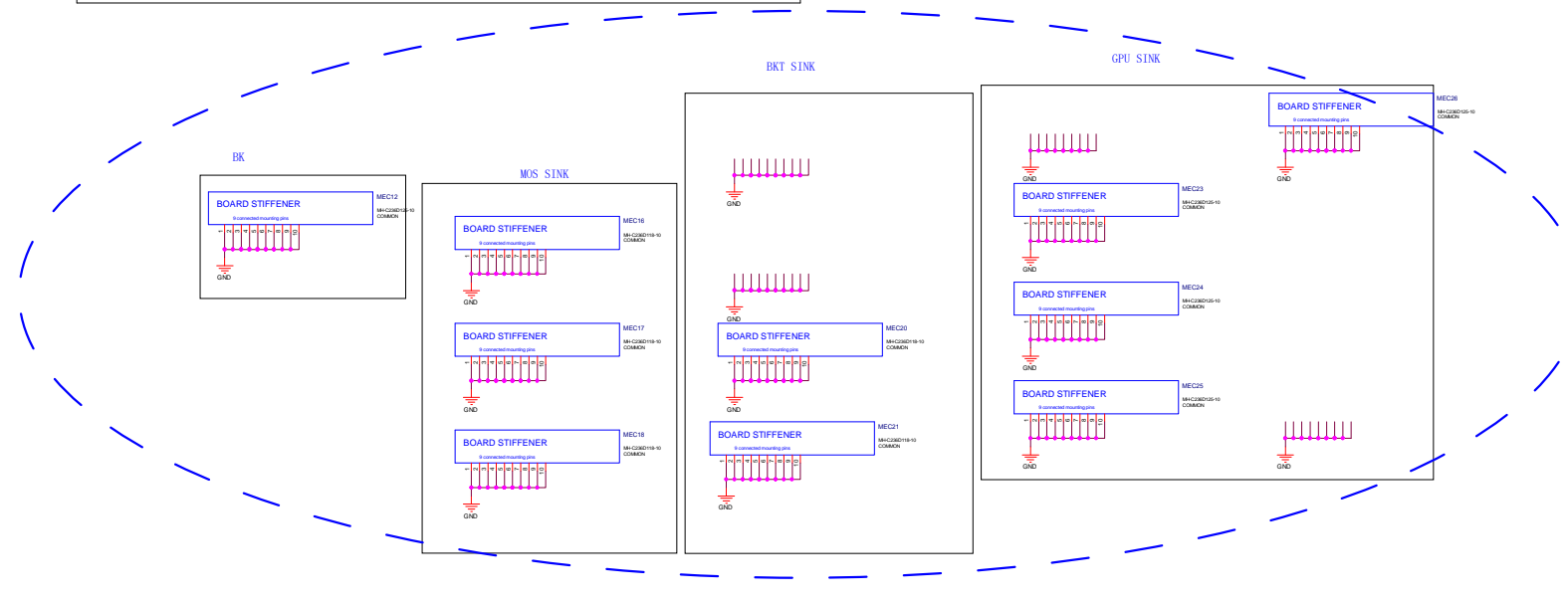
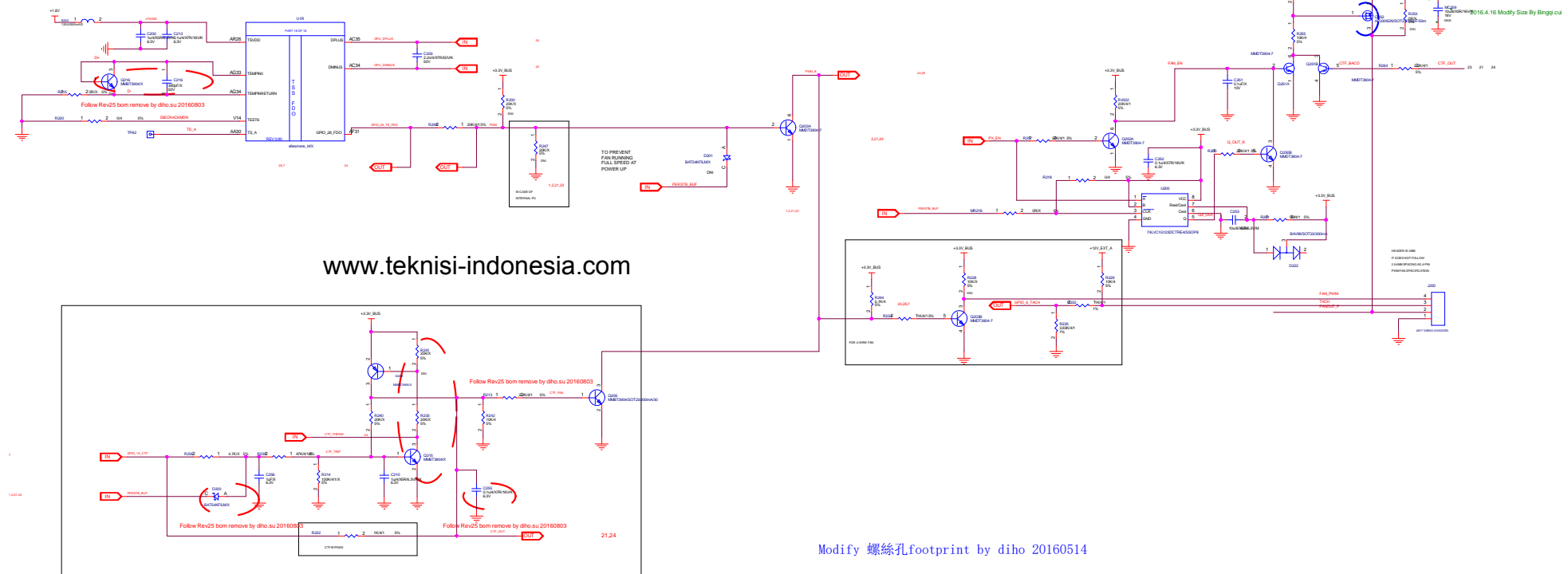
(19) POWER MANAGEMENT



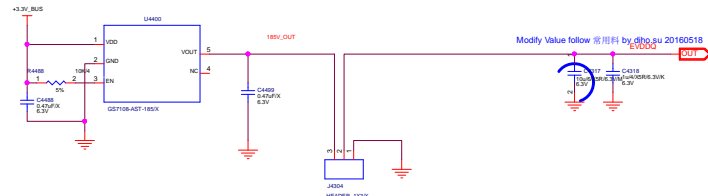
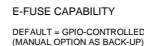
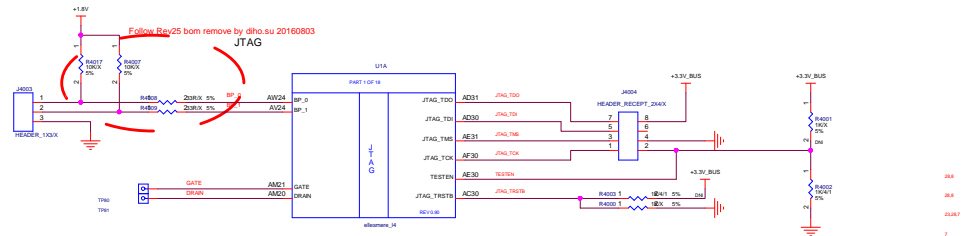
6PIN&8PIN CONNECT COLAY by diho 20160514



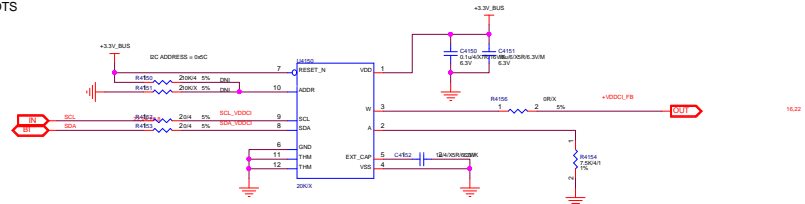
(20) MECHANICAL AND THERMAL MANAGEMENT



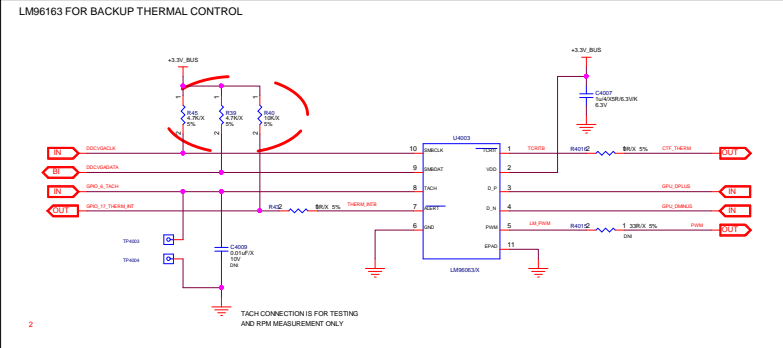
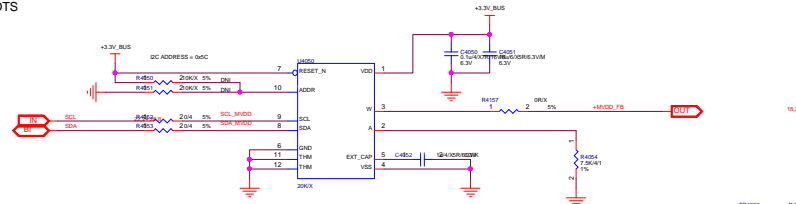
(21) DEBUG CIRCUITS



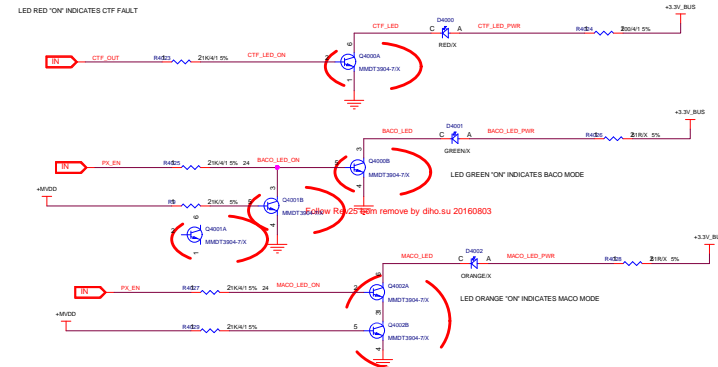
DIGITAL POTS



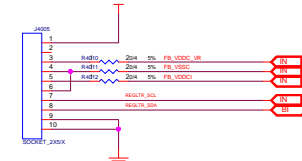
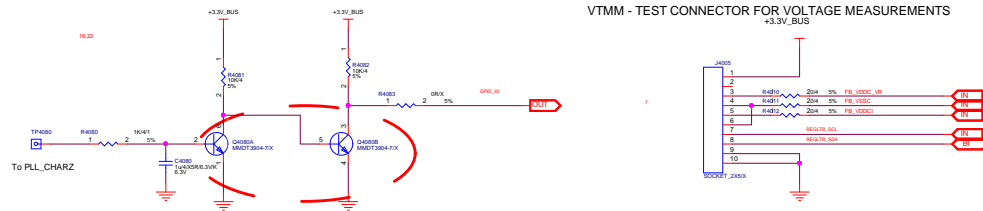
DIGITAL POTS



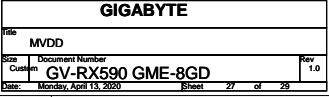
LED LIGHTS

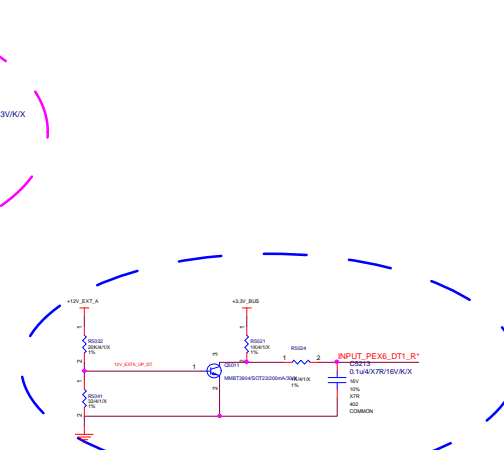
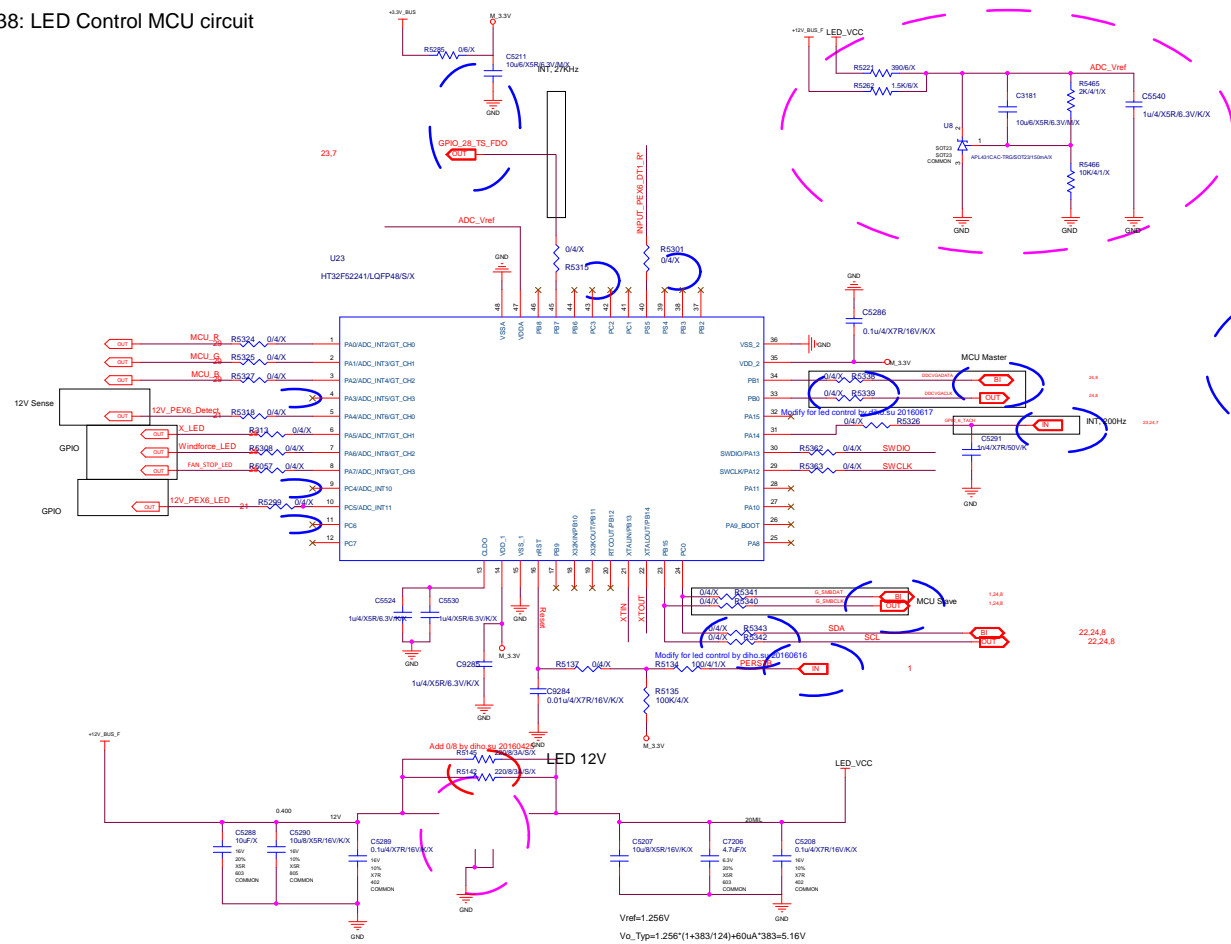


VTMM - TEST CONNECTOR FOR VOLTAGE MEASUREMENTS

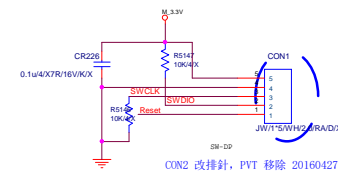
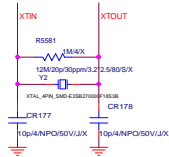


0	ISA	00000010	
1	ISA	00100010	1. Add SPIC1 to signature, 10000 2. update 00001 clear signature
2	ISC	00000010	PCBn: --add serials resistor R1580 -R1587 --add pull-down inductor L1580 -L1587 remove C405, C406, VR400, C410, C414, R405
3	ISC	00100010	Modify GET Design

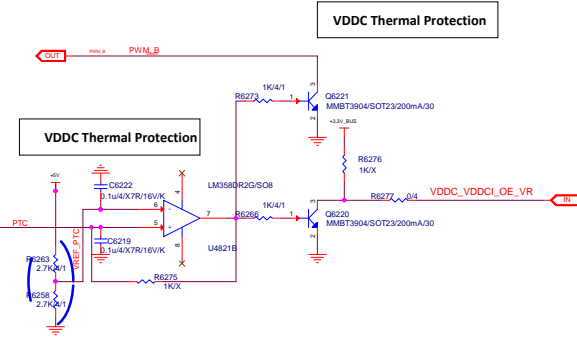
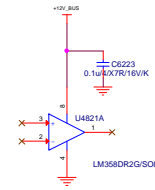
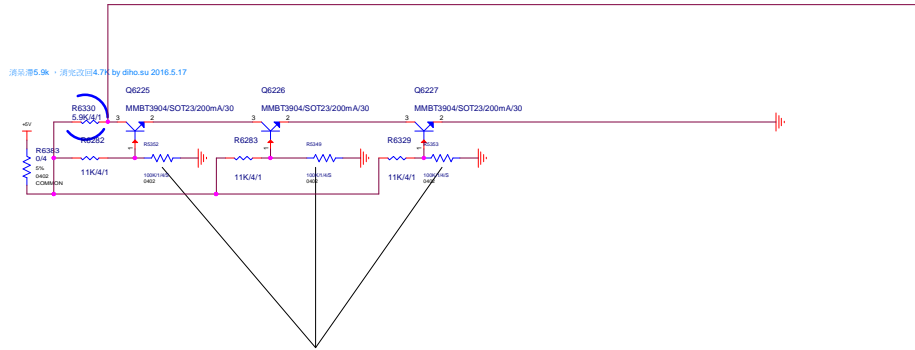




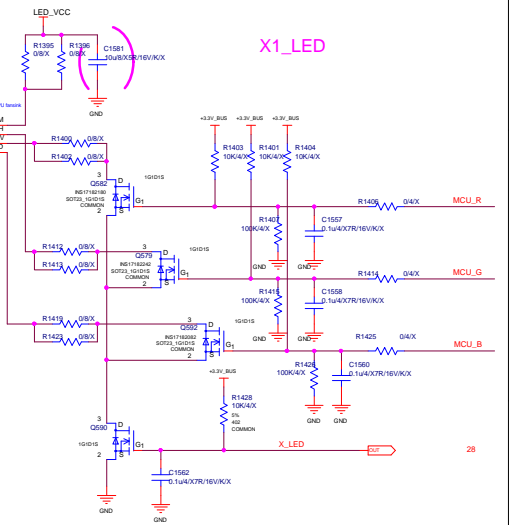
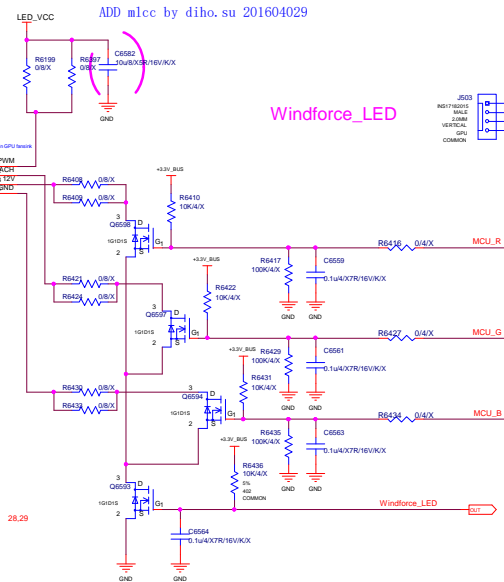
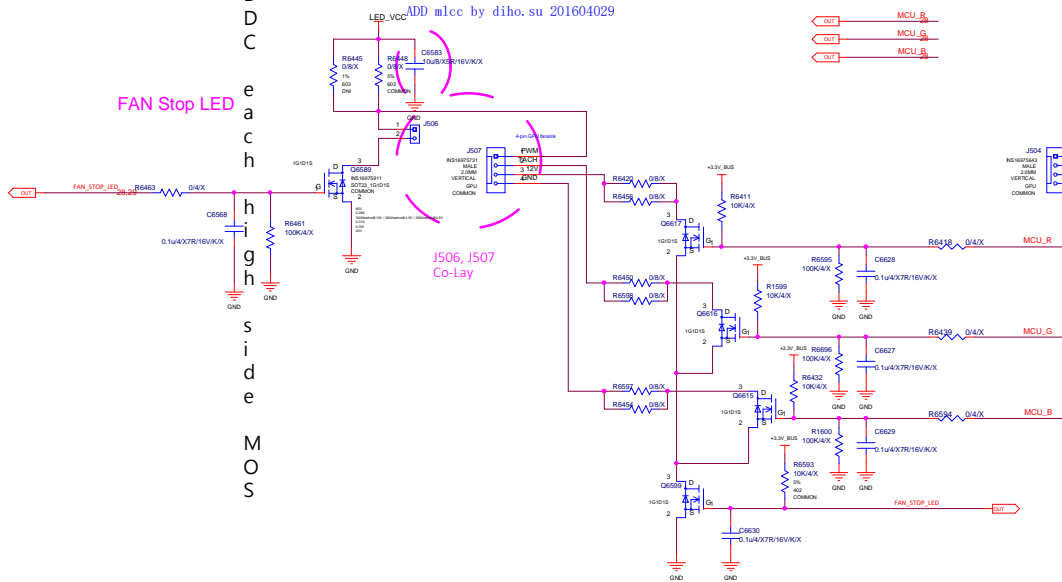
Vout = 8 ~ 7.5V



Add MOS VRHOT CIRCUIT



Near VDDC each side MOS



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PAGE DETAIL

MECH Bracket/Thermal

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